MAGNETIC RECORDING AND/OR REPRODUCING

Greer, Burns & Crain, Ltd. (James K. Folk Ref. No. 0941.65729 Sheet 1 of 25 (312) 360 0080

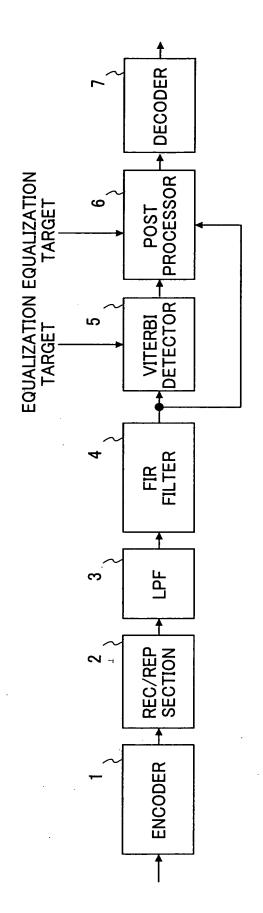
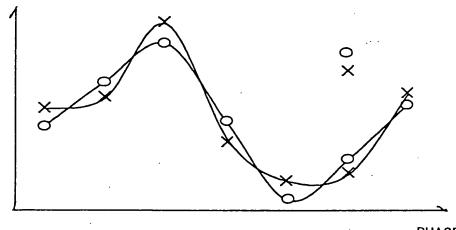




FIG. 2

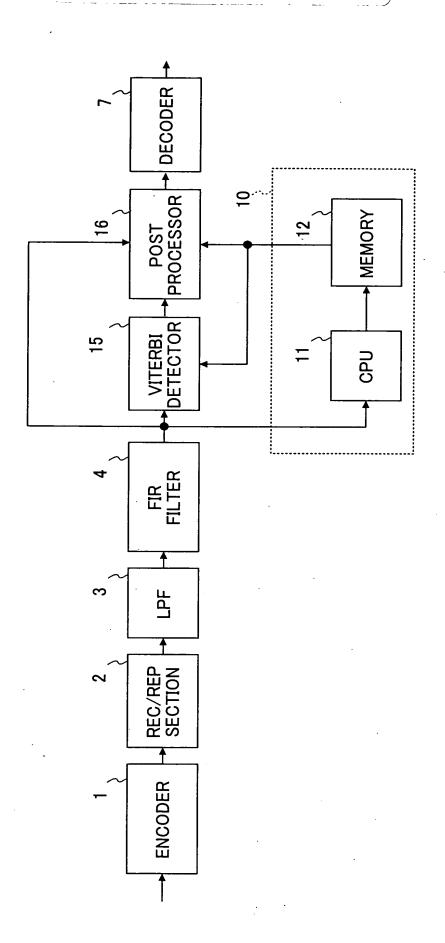


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PHASE

FIG.3



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FIG.4

	EQUALIZA	ATION OUTPUT bk				
ak-3ak-2ak-1ak	IDEAL EQUALIZATION VALUE	AVERAGE VALUE OF ACTUAL EQUALIZED WAVEFORM				
0000	0	μο				
0001	+1	μ 1				
0010	+1	μ 2				
0011	+2	μз				
0100	-1	μ 4				
0101	0	μ 5				
0110	0	μ 6				
0111	+1	μ 7				
1000	-1	μ 8				
1001	. 0	μ 9				
1010	0	μ 10				
1011	+1	µ 11				
1100	-2	μ 12				
1101	-1	μ 13				
1110	-1	µ 14				
1111	0	µ 15				



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FIG.5

ak-2ak-1ak	STATE
000	So
001	S ₁
010	S ₂
011	S₃
100	S ₄
· 101	S 5
110	S ₆
111	S7

MAGNETIC RECORDING AND/OR REPRODUCING Ichihara et al.

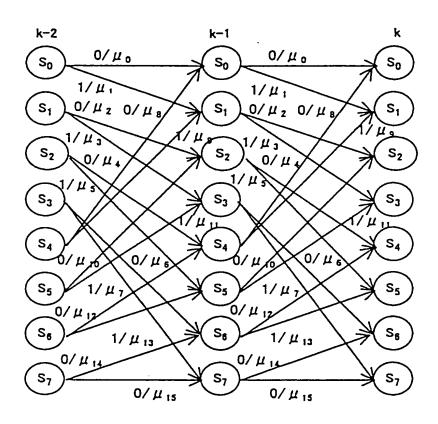
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JT bk	T bk UE OF ZED:::.	1	μ 1	Д 3	<u>μ</u> 5	τ μ	6 <i>T</i> /	μ 11	Д 13	J 15
PRESENT EQUALIZATION OUTPUT bk	AVERAGE VALUE OF ACTUAL EQUALIZED WAVEFORM	0	0 η	Д 2	Д 4	д в	И 8	η 10	μ 12	H 14
ALIZAT AV ACTI	ă									
ENT EQU	ALUE	-	7	+5	0	+	0	+	ī	0
PRESE	PRESENT E		0	Ŧ	ī	0	T	0	-2	T
	11	ā								•
	PRESENT STATE		S.	Š	Ss	S ₇	S.	Ŝ	Š	S ₇
			Ŝ	S_2	S ₄	Se	So	S_2	S ₄	S
1										
	STATE OF 1 BIT BEFORE		ගි	Ñ	S ₂	တိ	δ,	Ş	တိ	S,

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FIG. 7



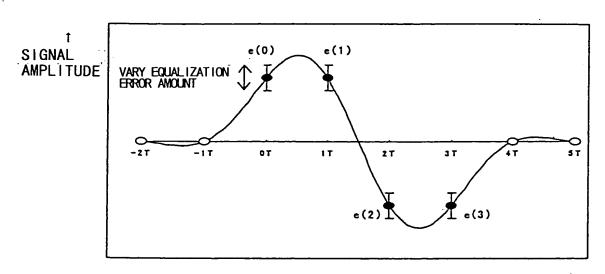
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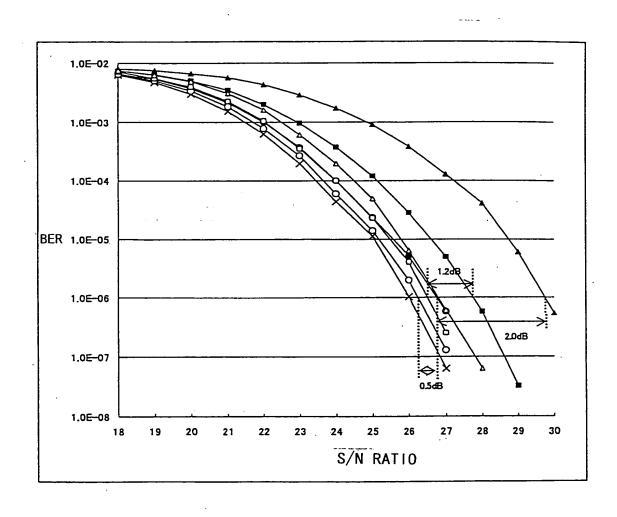
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FIG. 8



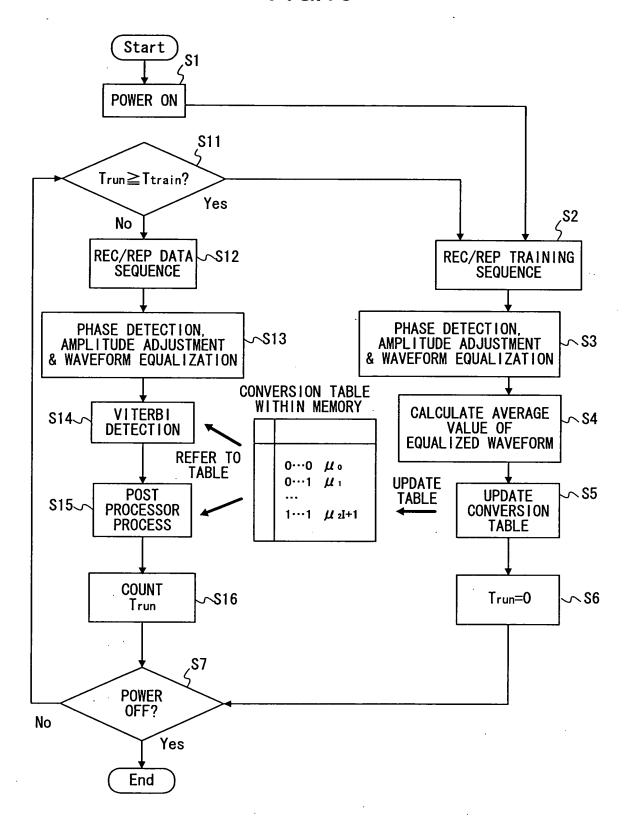
TIME

FIG. 9



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FIG.10



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FIG.11

ak-2	a k-1	ak	COMPENSATION AMOUNT
0	0	1	_
0	1	1	T ₀₁
1	0	1	T10
1	1	1	T11

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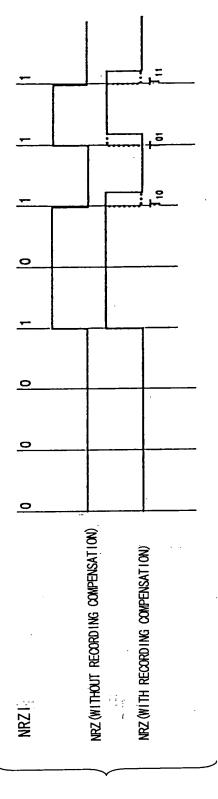
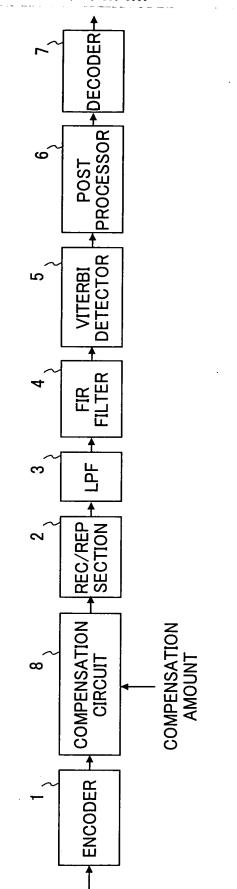


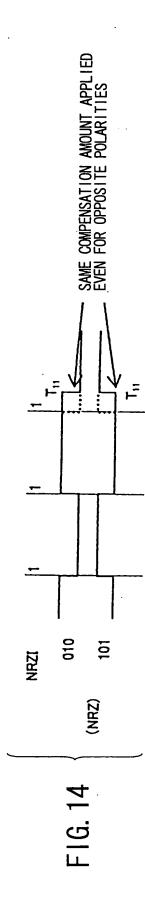
FIG. 12

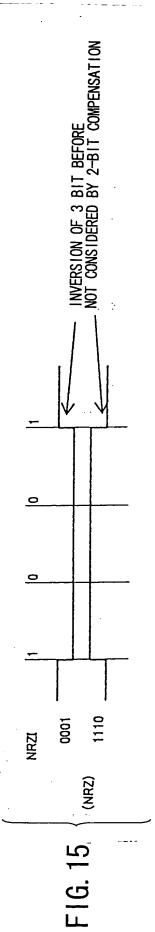
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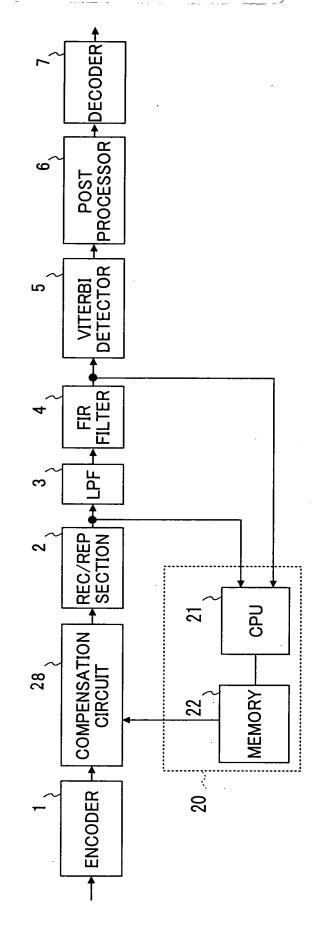
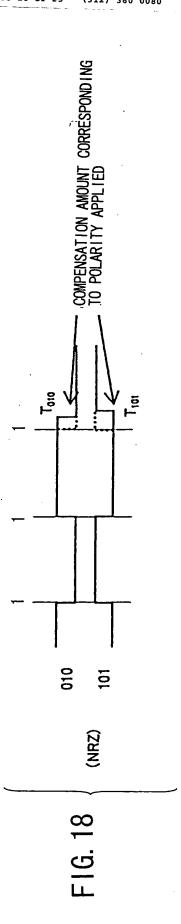


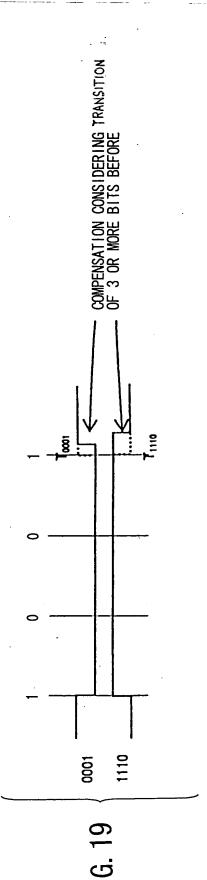
FIG.16

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FIG. 17

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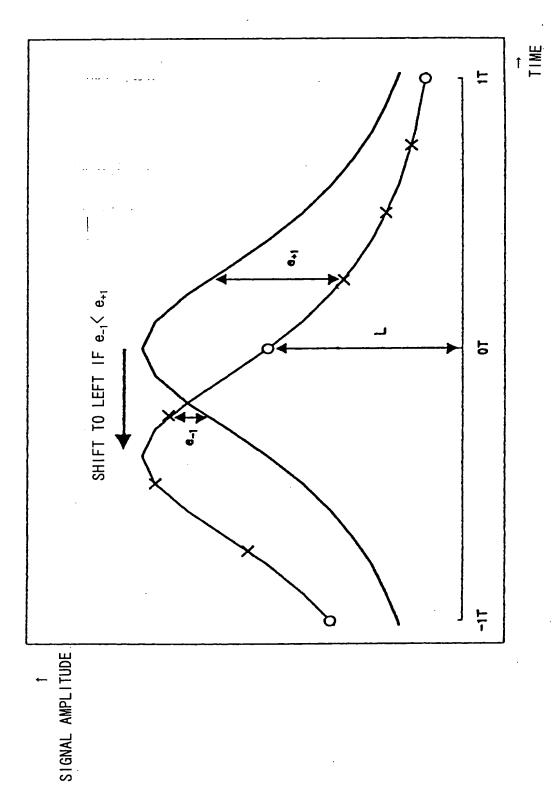


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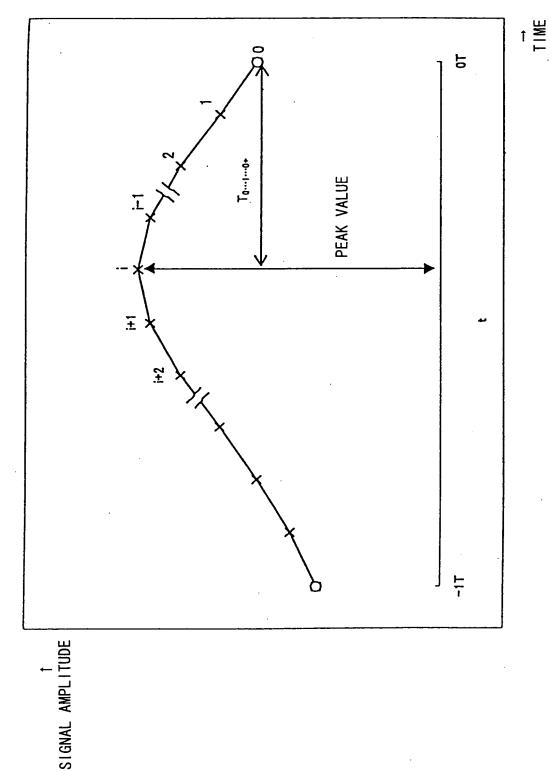


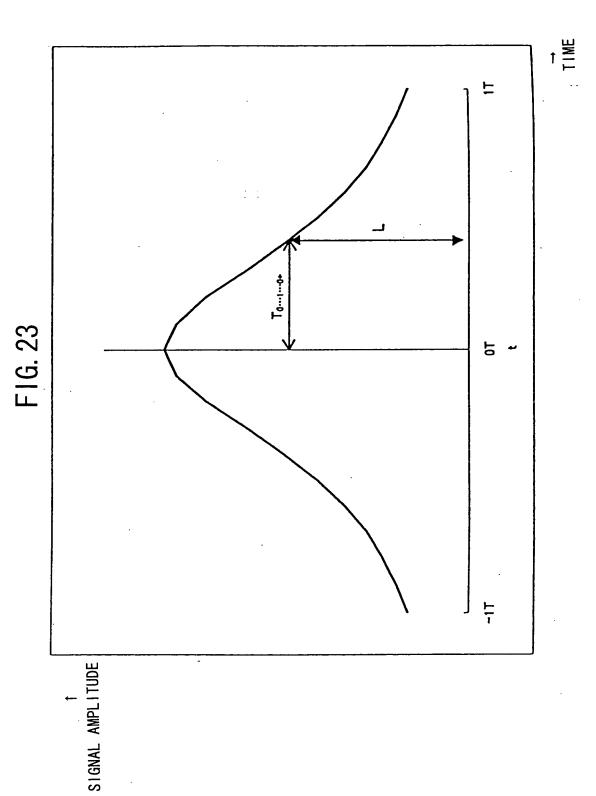
	•	•	•	•		•	•	•	•	·
	:	:	:	:	:	:		:	:	:
-SAMPLED -ORM	w(1)	W(1)010+	W(1) ₀ ···1···0-	W(1)011+	W(1) ₀ ···1···1-	:	W(1) ₁ ····0+	W(1) ₁ ₀ -	W(1)11+	W(1)111-
VER AVE		:		•	:	•	:	•	•••	:
AVERAGE VALUE w(t) (-1≦t≦1) OF OVER-SAMPLED REPRODUCED OR EQUALIZED WAVEFORM	w(0)	W(0)010+	W(0) ₀ ₀ -	W(0) ₀ ····1+	W(0) ₀ ····1		W(0)110+	W(0)110-	W(0)11.1+	W(0)1····1···1-
JE w(:	:	•		•	••	:	• • •	•••	:
AVERAGE VALU REPRODU	w(-1)	W(-1) ₀ ···1···0+	W(-1) ₀ ···· ₀ -	W(-1) ₀ ····1+	W(-1)011-	:	W(-1)110+	W(-1)110-	W(-1)11+	W(-1)11-1-
	POLARITY	+	ı	+	1	:	+	1	+	ı
RECORDING SEQUENCE	a E	c	0		>	•	0			
	:		•			•	•		:	
	ð	-	_	T	_		-	_	•	_
REC SE(•					•				
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F1G. 21



F1G. 22





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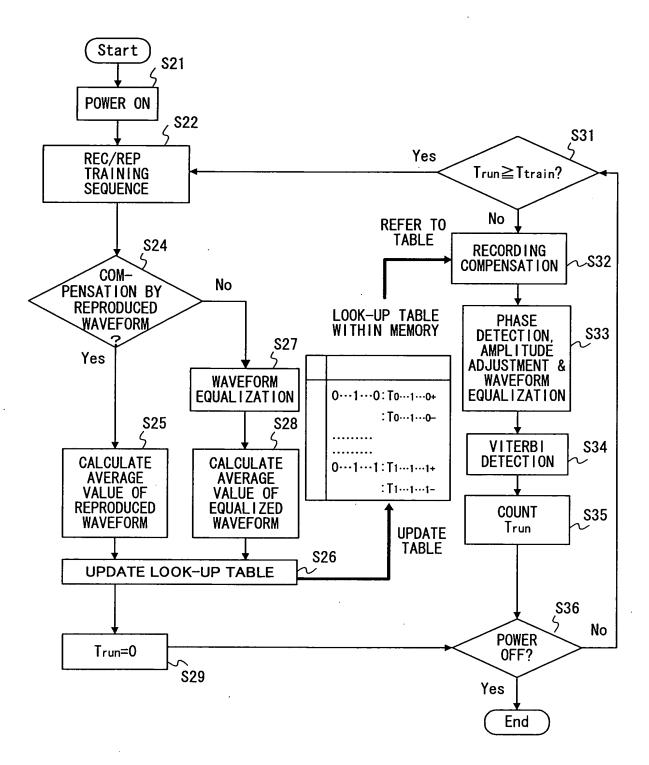
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FIG.24





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DECODER -PROCESSOR 9 MEMORY VITERBI DETECTOR 5 SPU 2 FIR FILTER LPF REC/REP SECTION COMPENSATION CIRCUIT ₂₈ ENCODER

FIG 25